

FIG. 1a
Shared-Memory Processor Cluster

10a

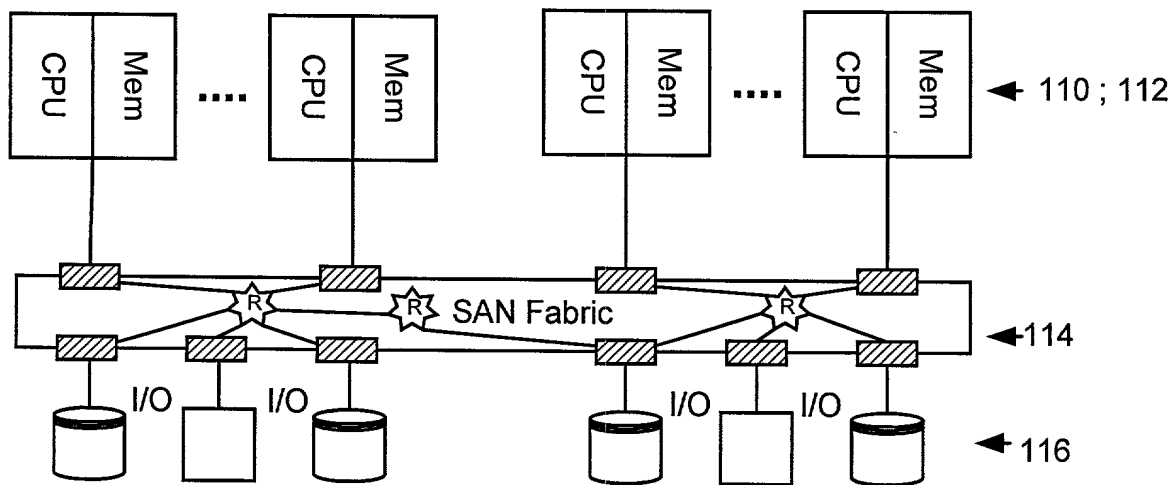


FIG. 1b
Shared Noting Parallel Processors Cluster

10b

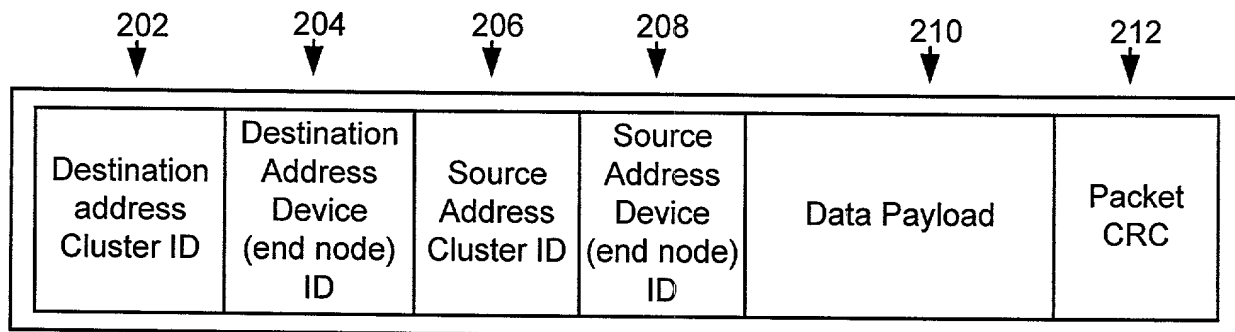
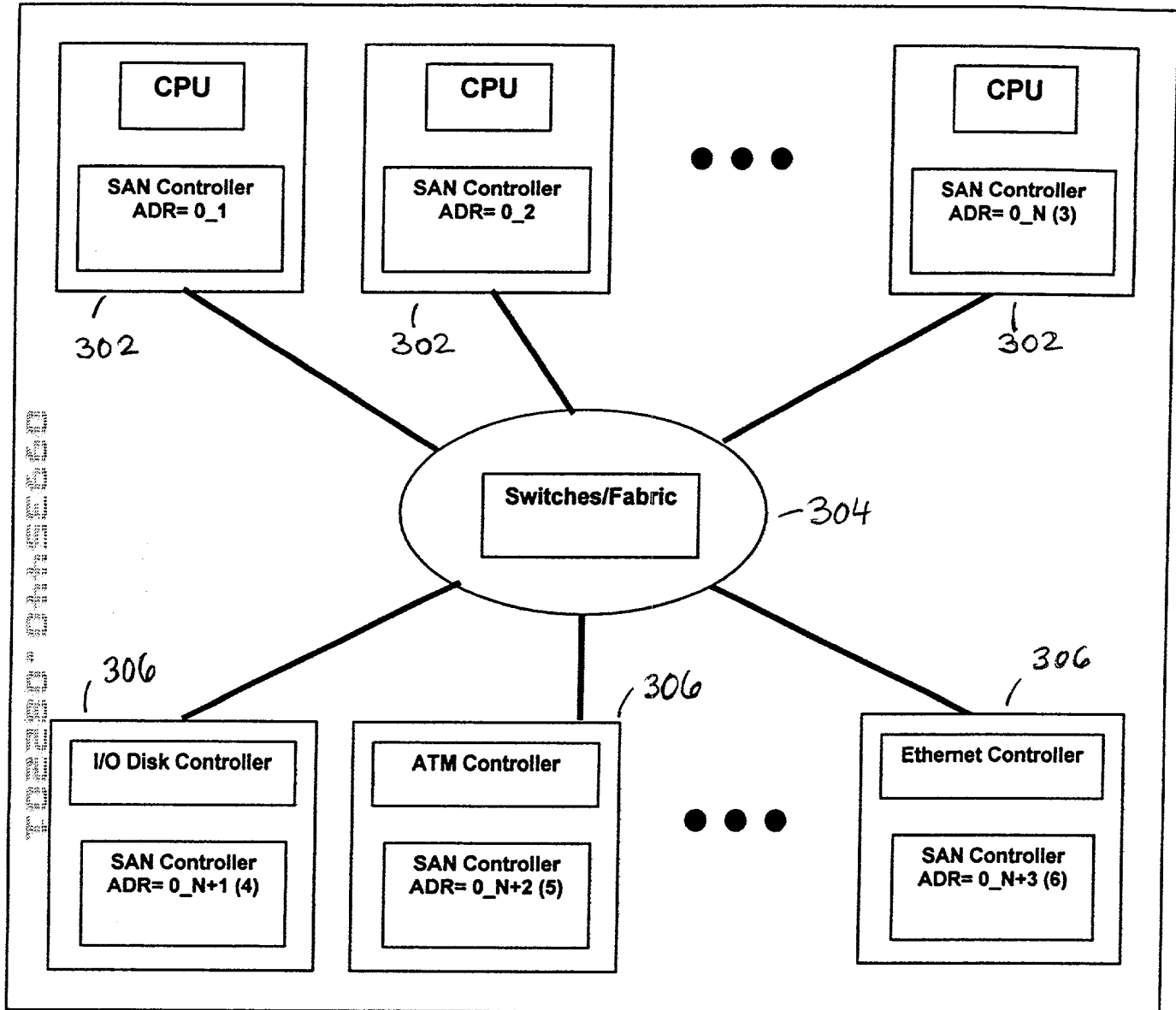


FIG. 18
Header Format

200

Figure 2. Cluster Node



300

Figure 3. Flat or Non-Hierarchical Growth

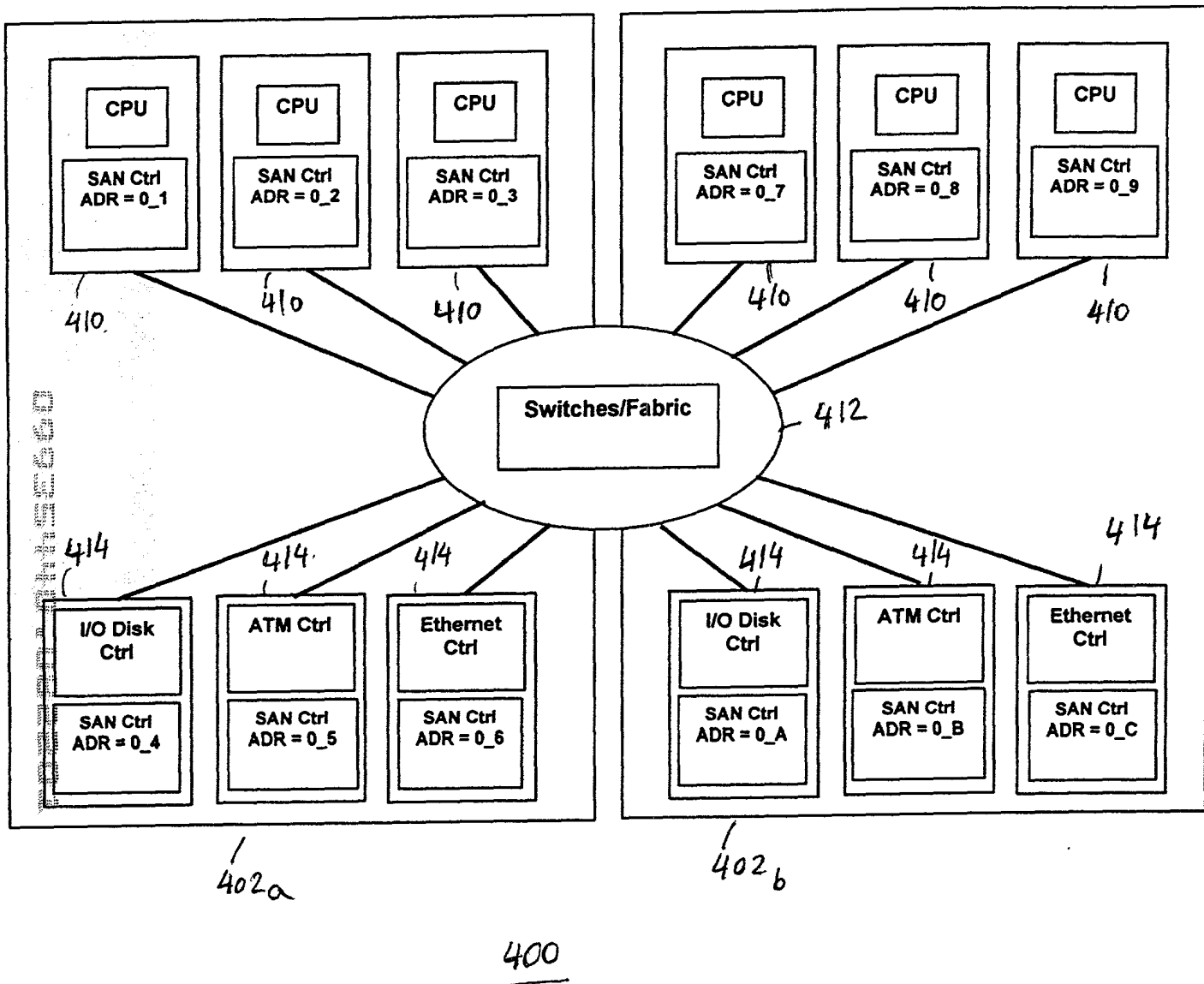


Figure 4. Hierarchical Growth

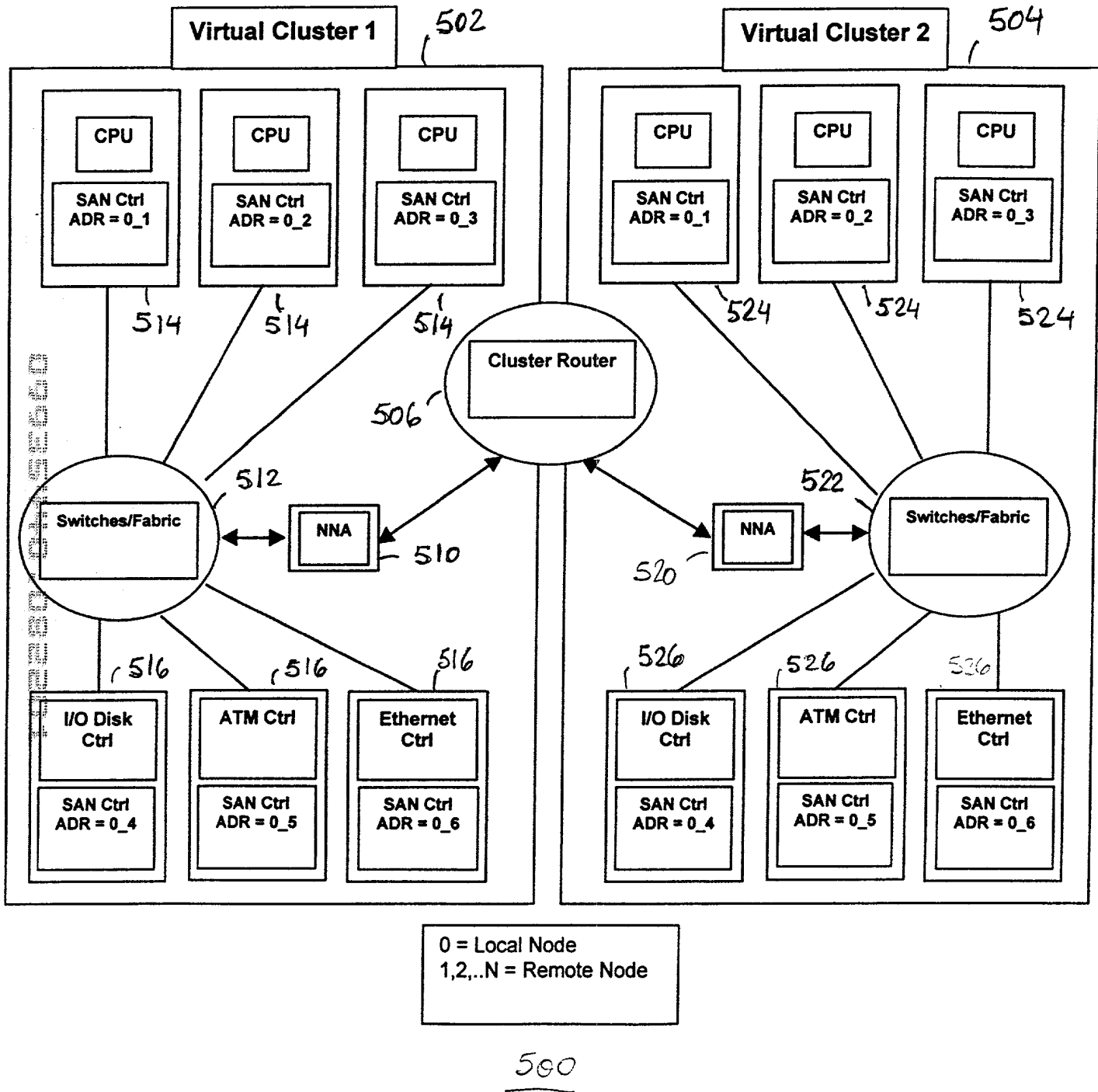
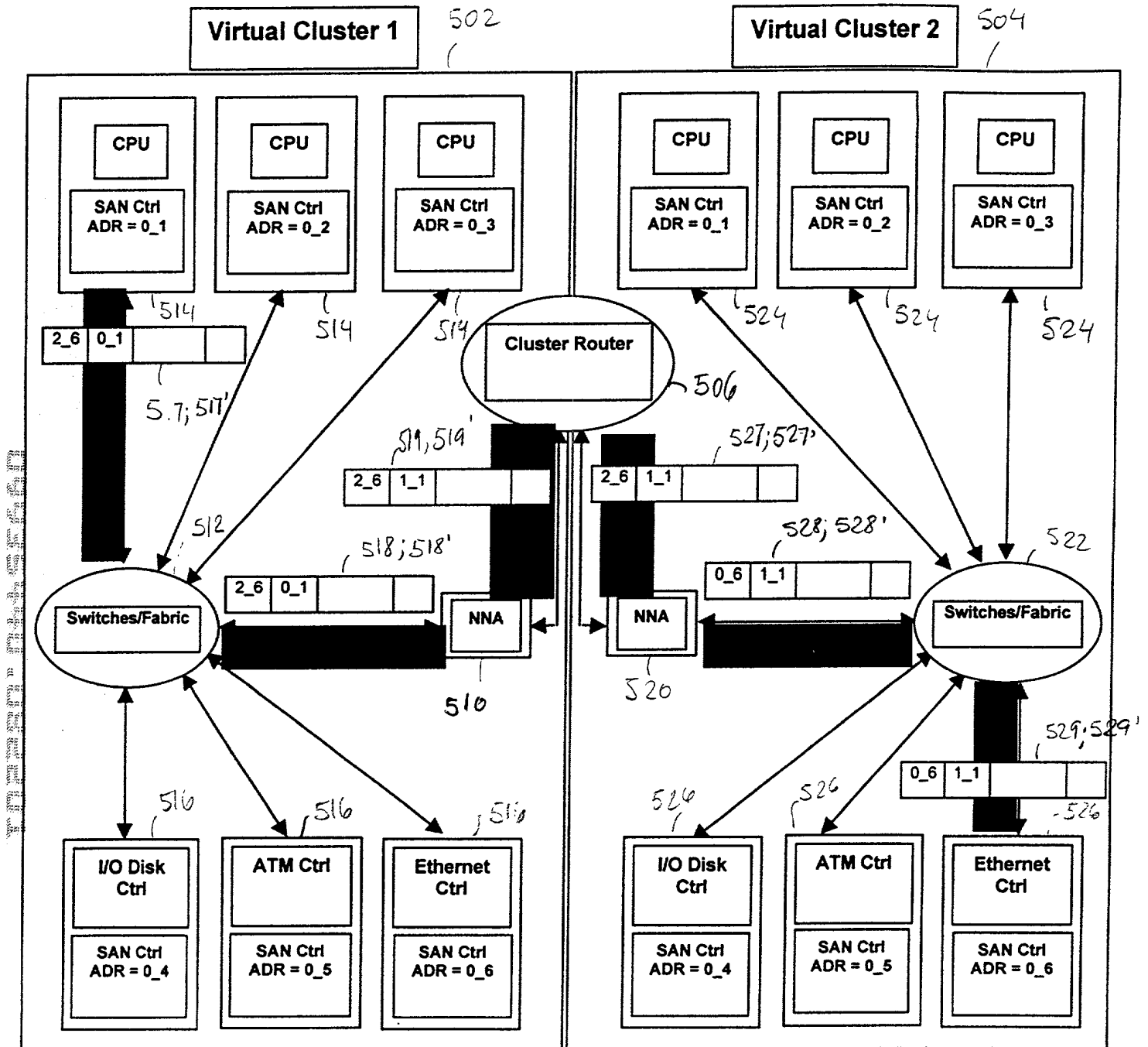


Figure 5. Full Packet Flow



500

Figure 6. Packet Flow – Step 1

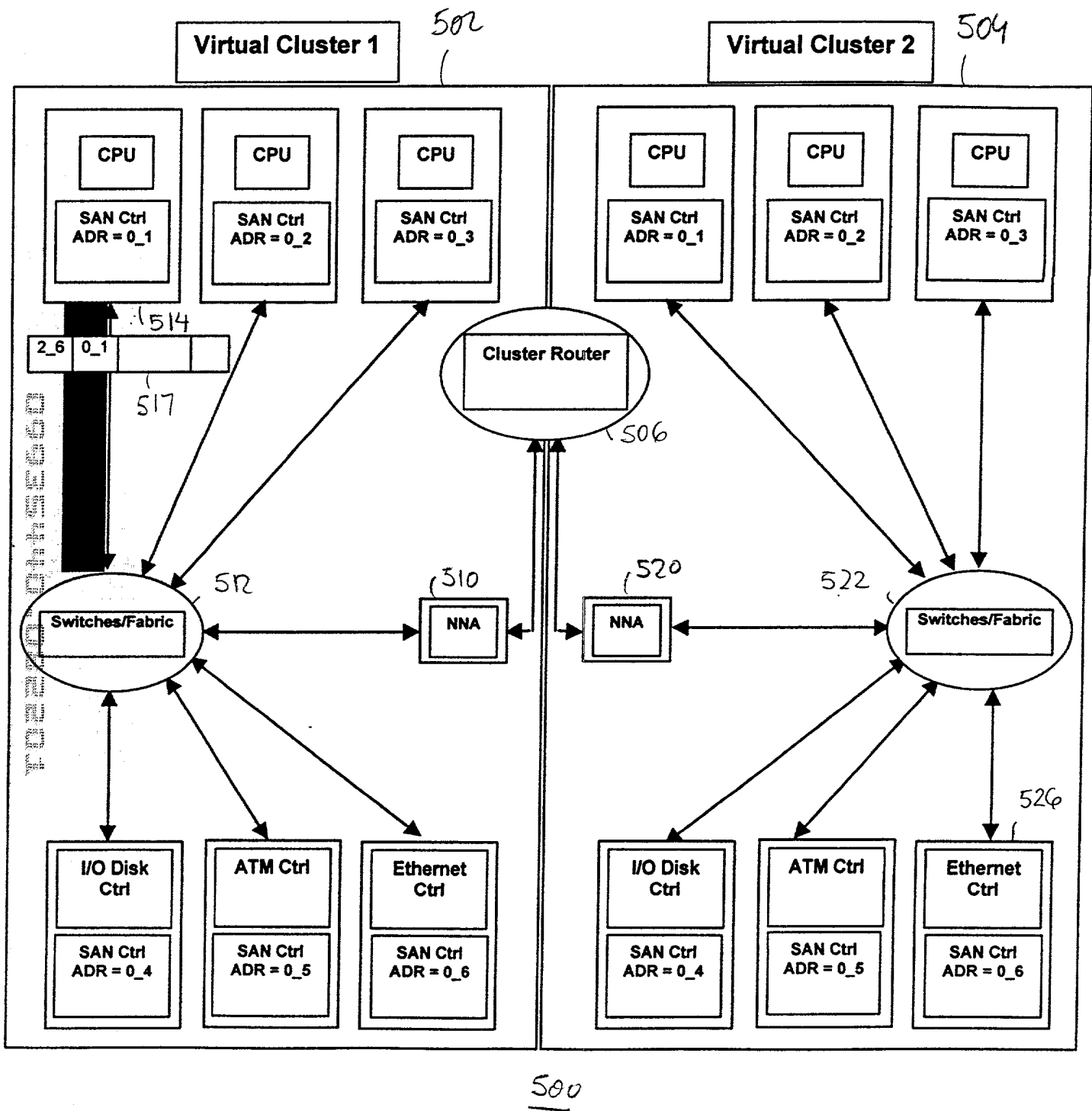
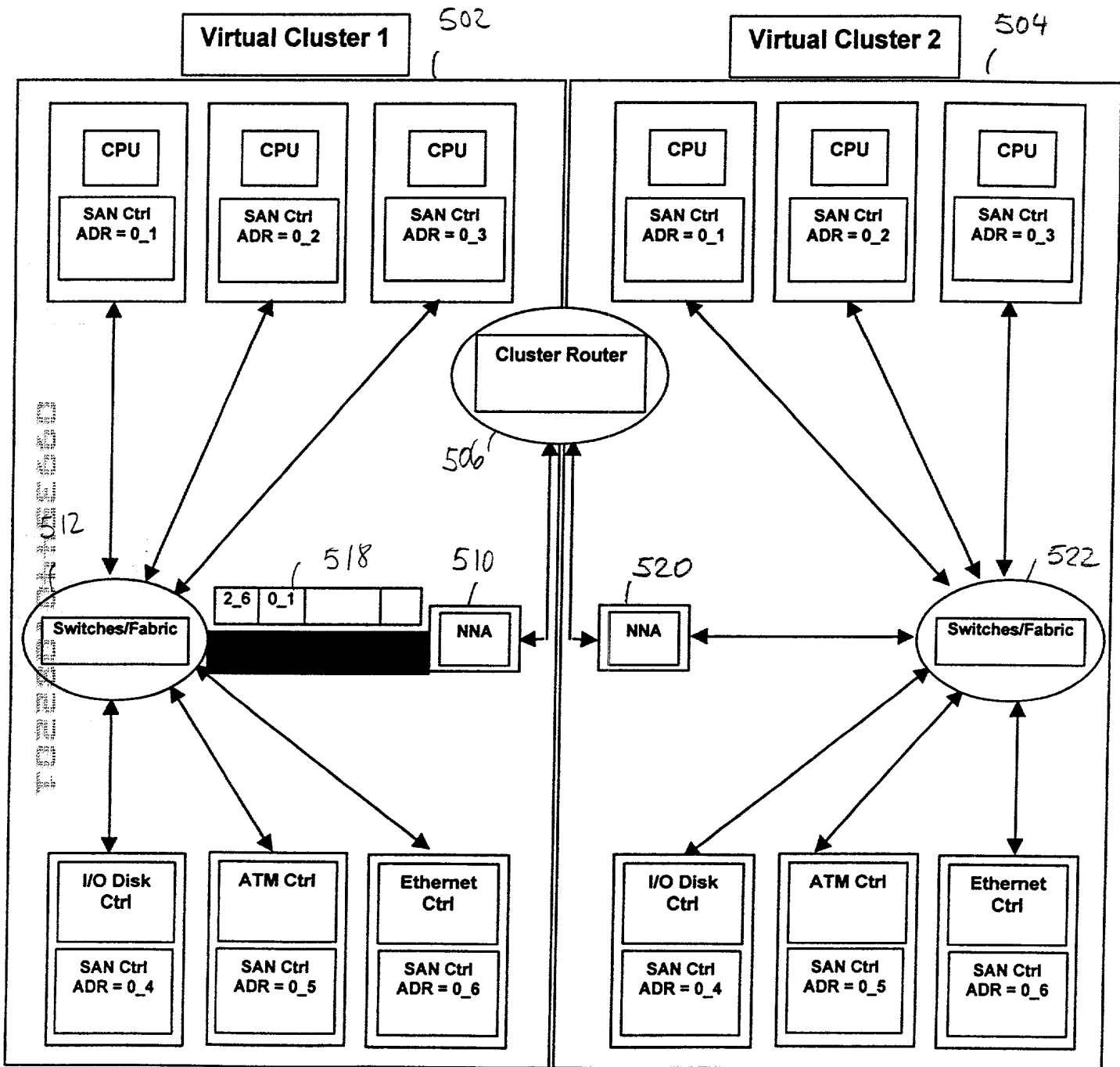


Figure 7. Packet Flow – Step 2



500

Figure 8. Packet Flow – Step 3

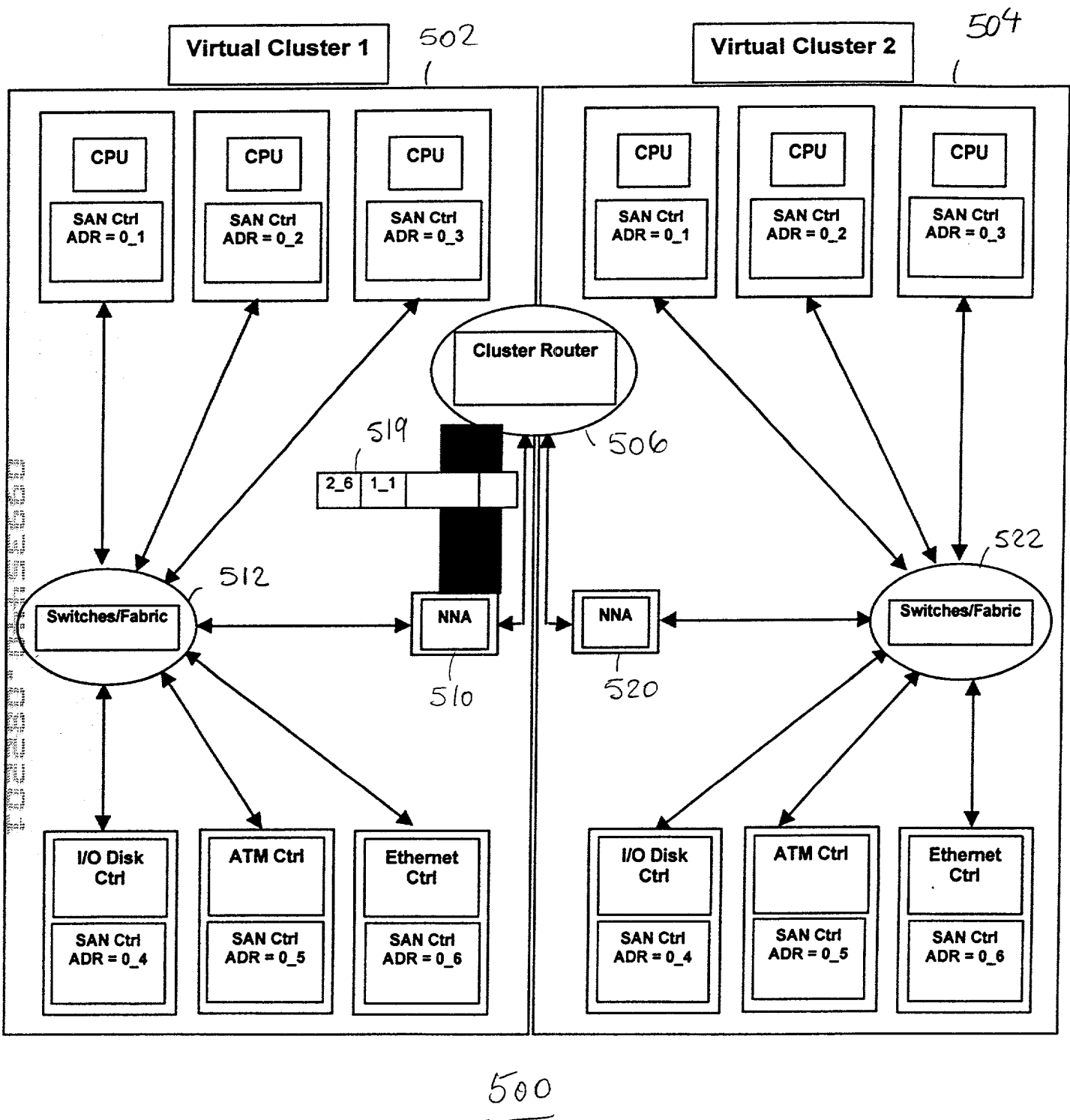


Figure 9. Packet Flow – Step 4

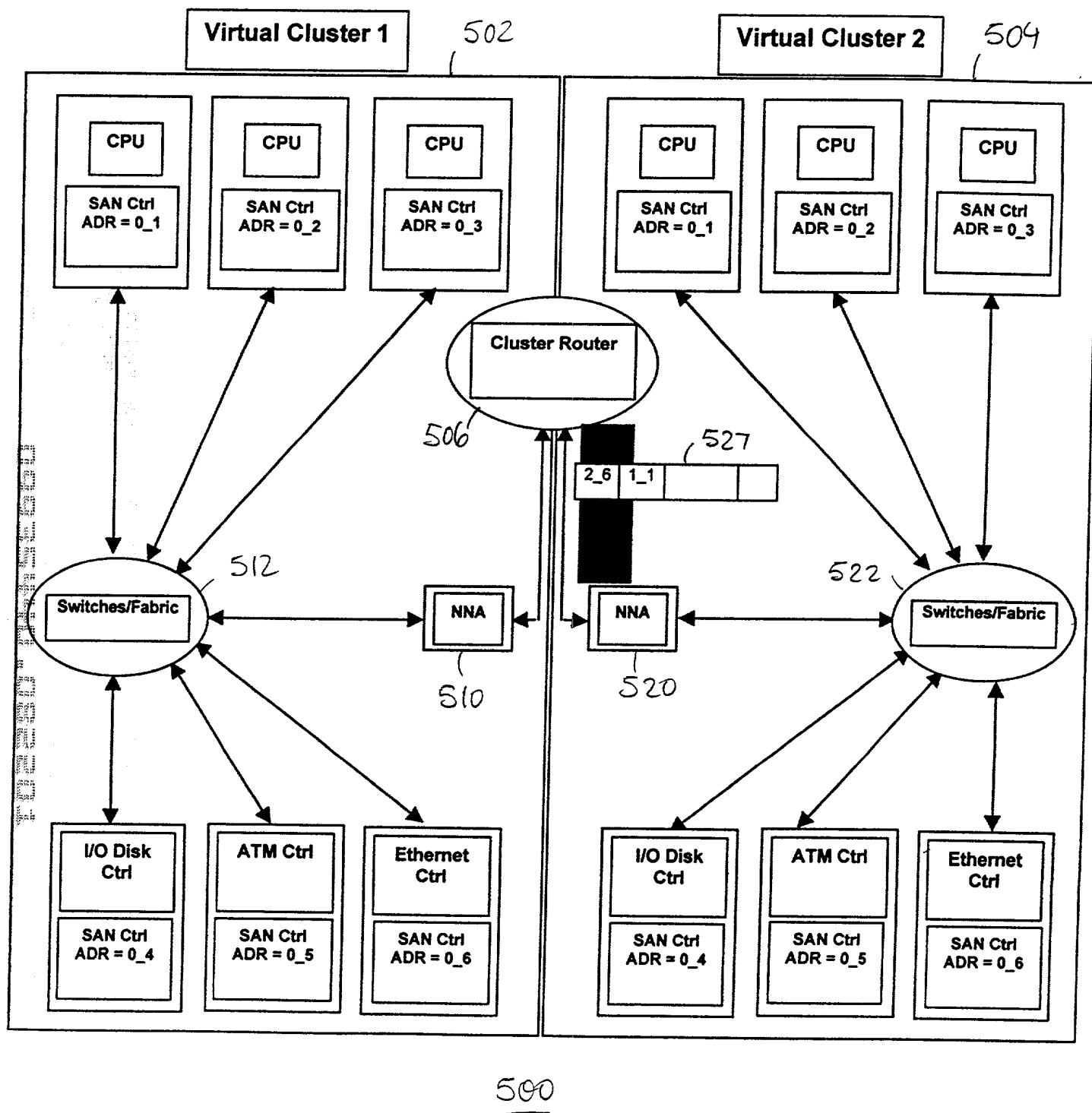
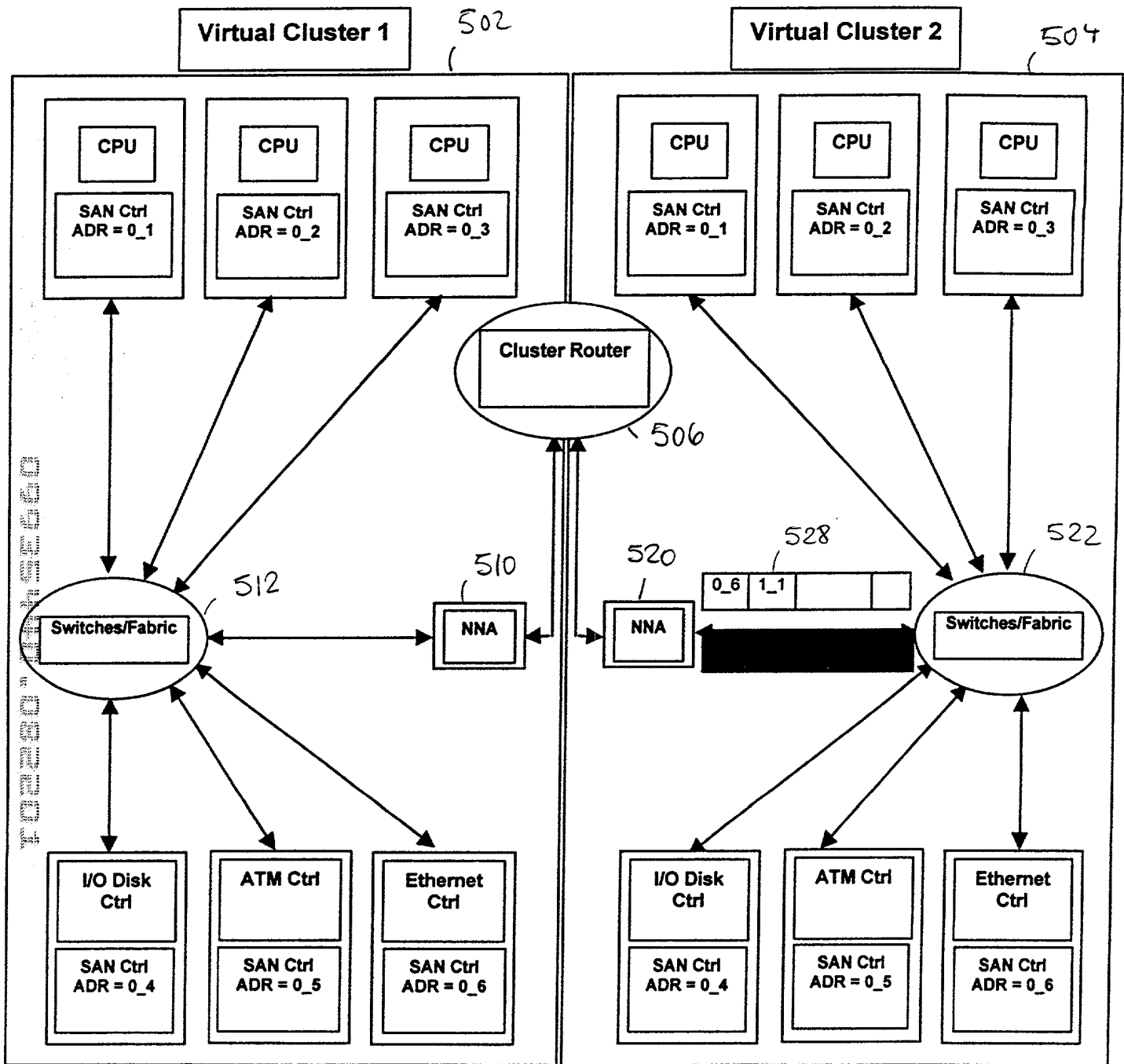
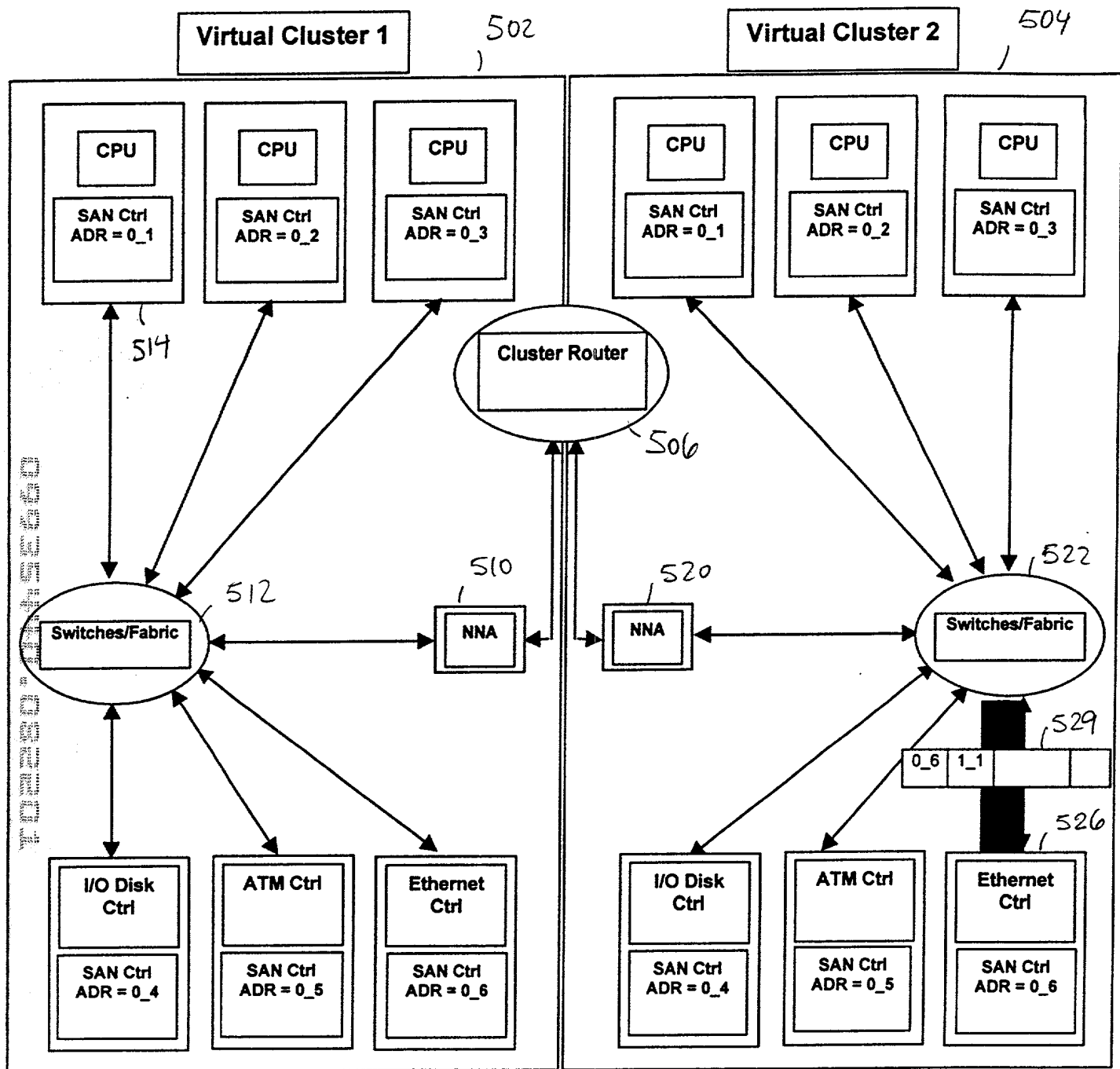


Figure 10. Packet Flow – Step 5



500

Figure 11. Packet Flow – Step 6



500

Figure 12. Response Packet Flow – Step 1

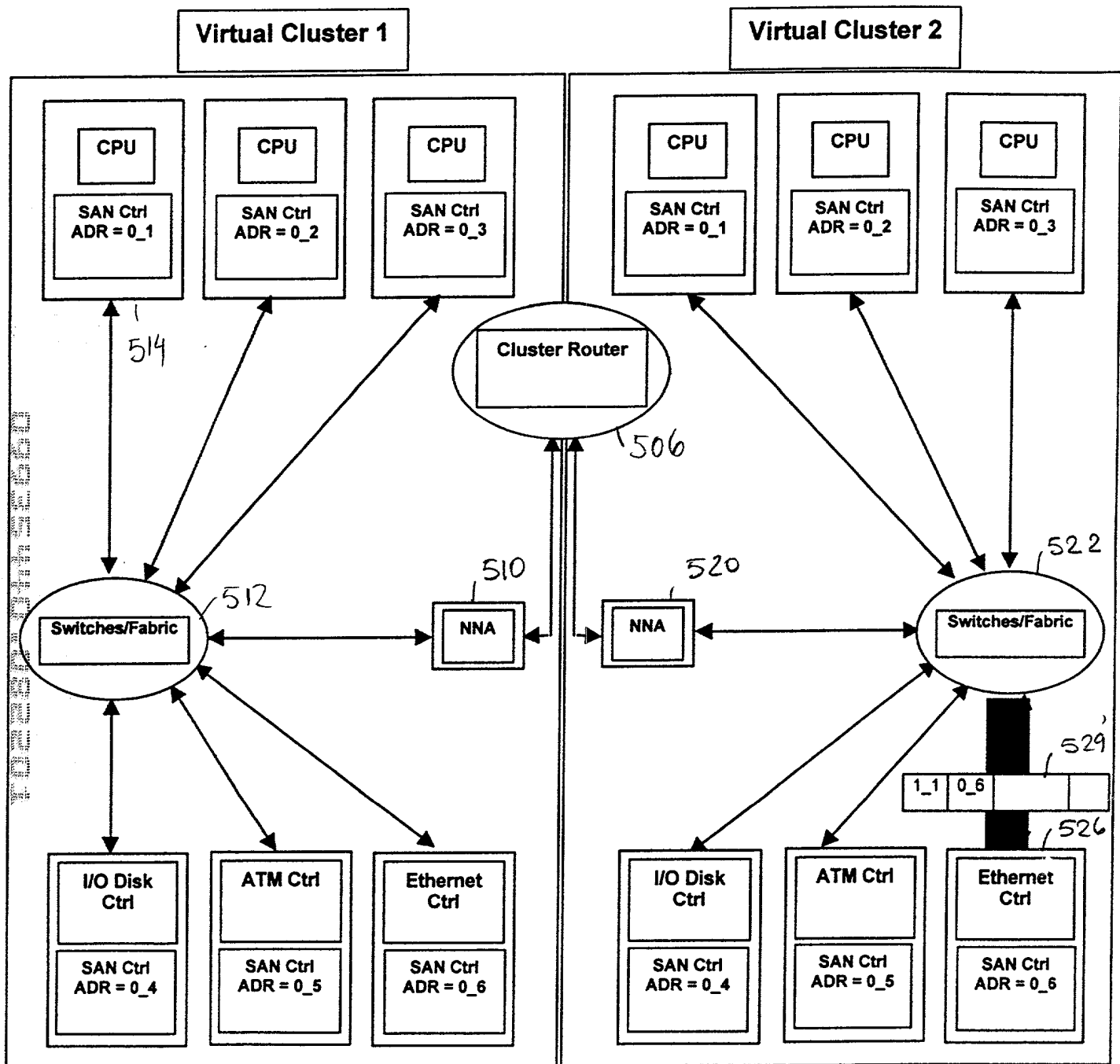


Figure 13. Response Packet Flow – Step 2

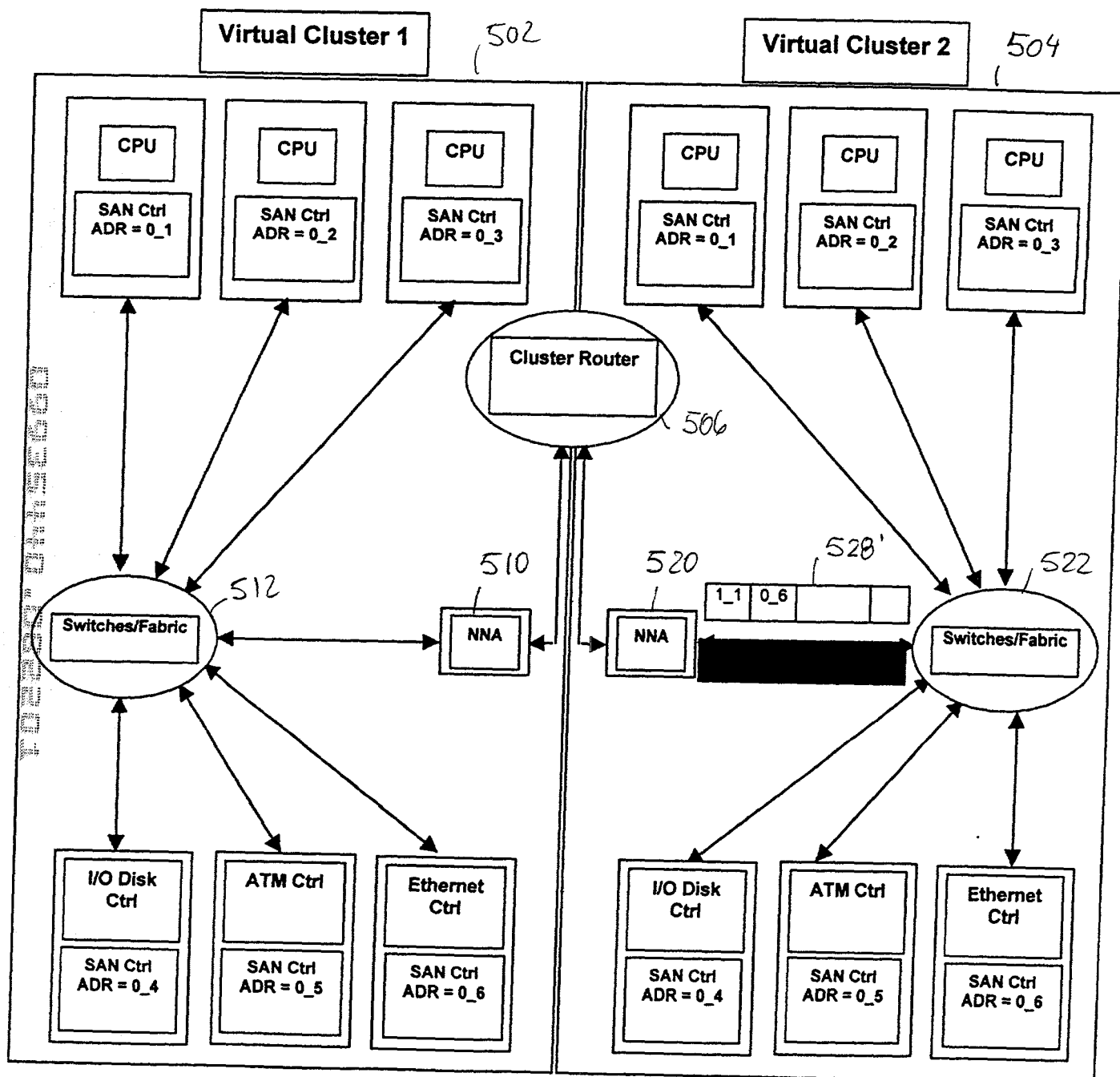


Figure 14. Response Packet Flow – Step 3

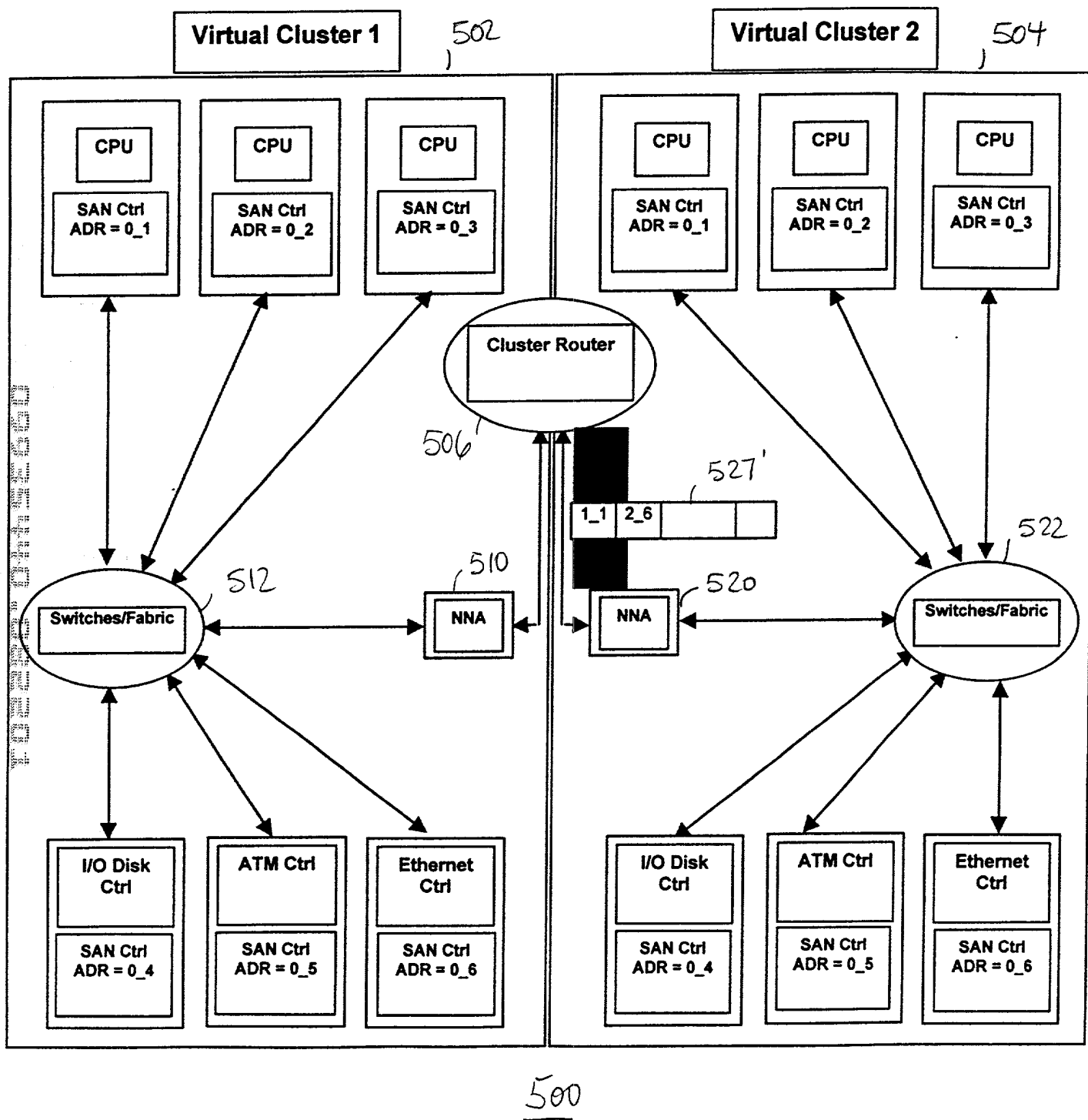


Figure 15. Response Packet Flow – Step 4

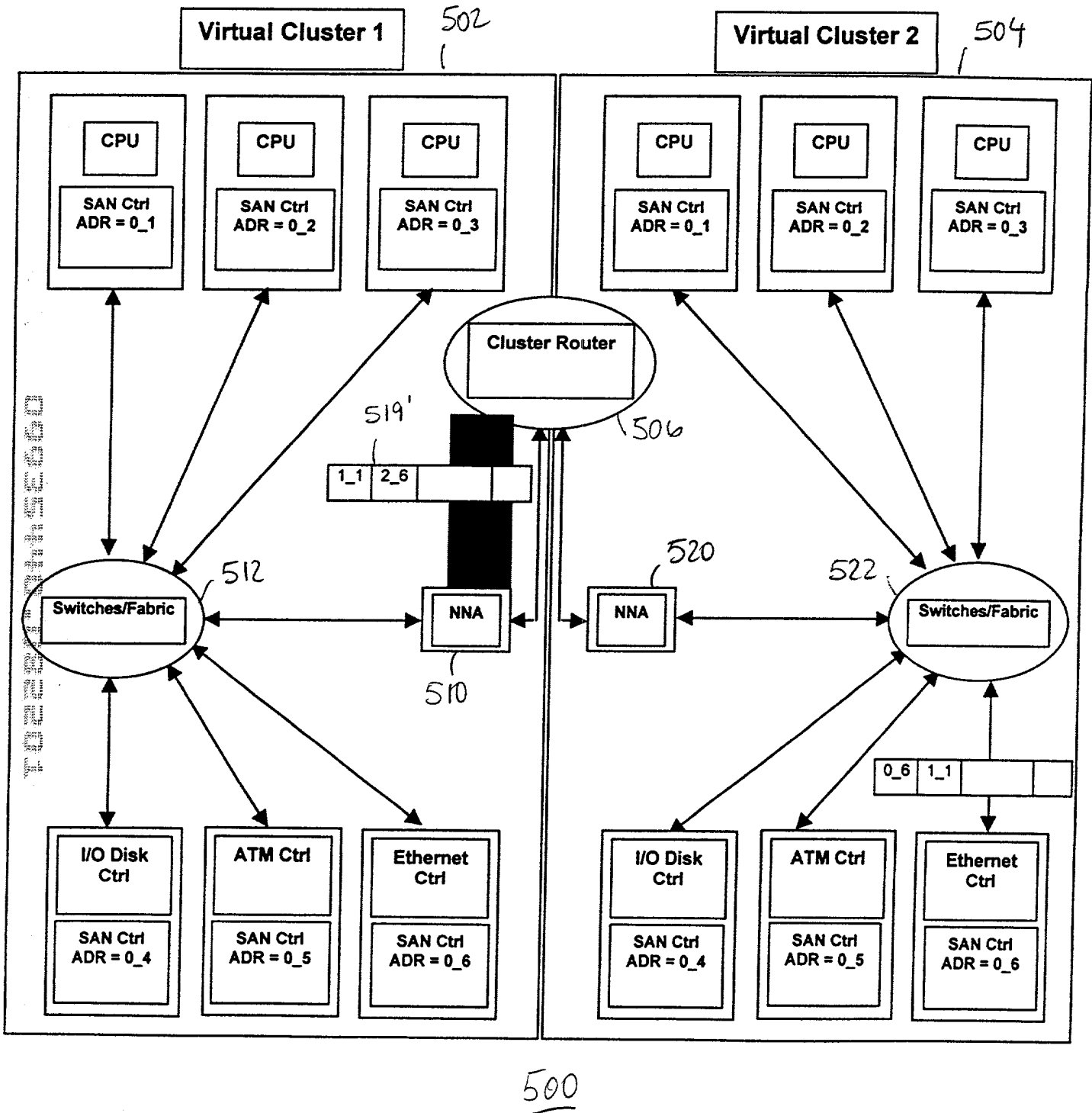


Figure 16. Response Packet Flow – Step 5

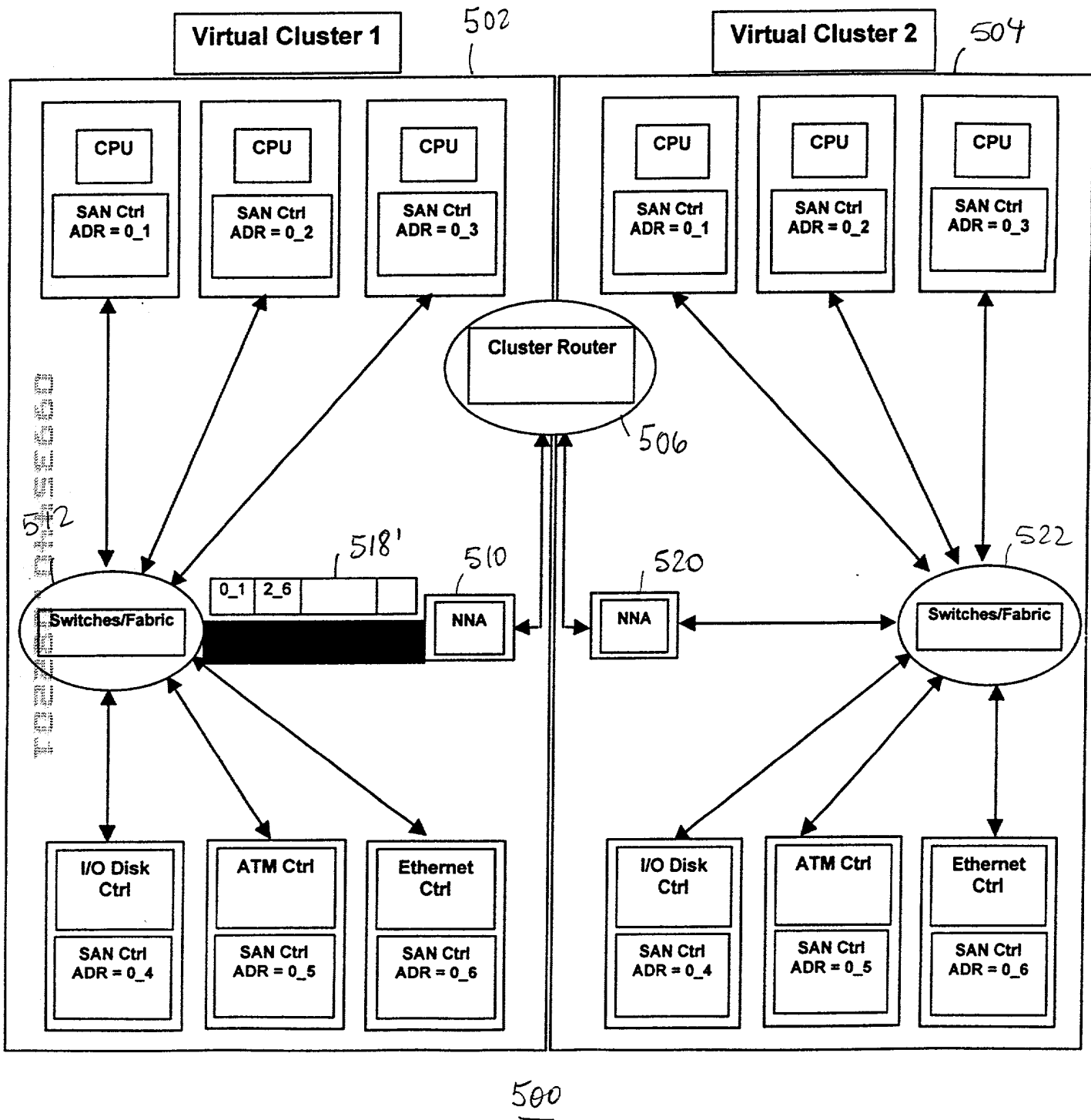


Figure 17. Response Packet Flow – Step 6

